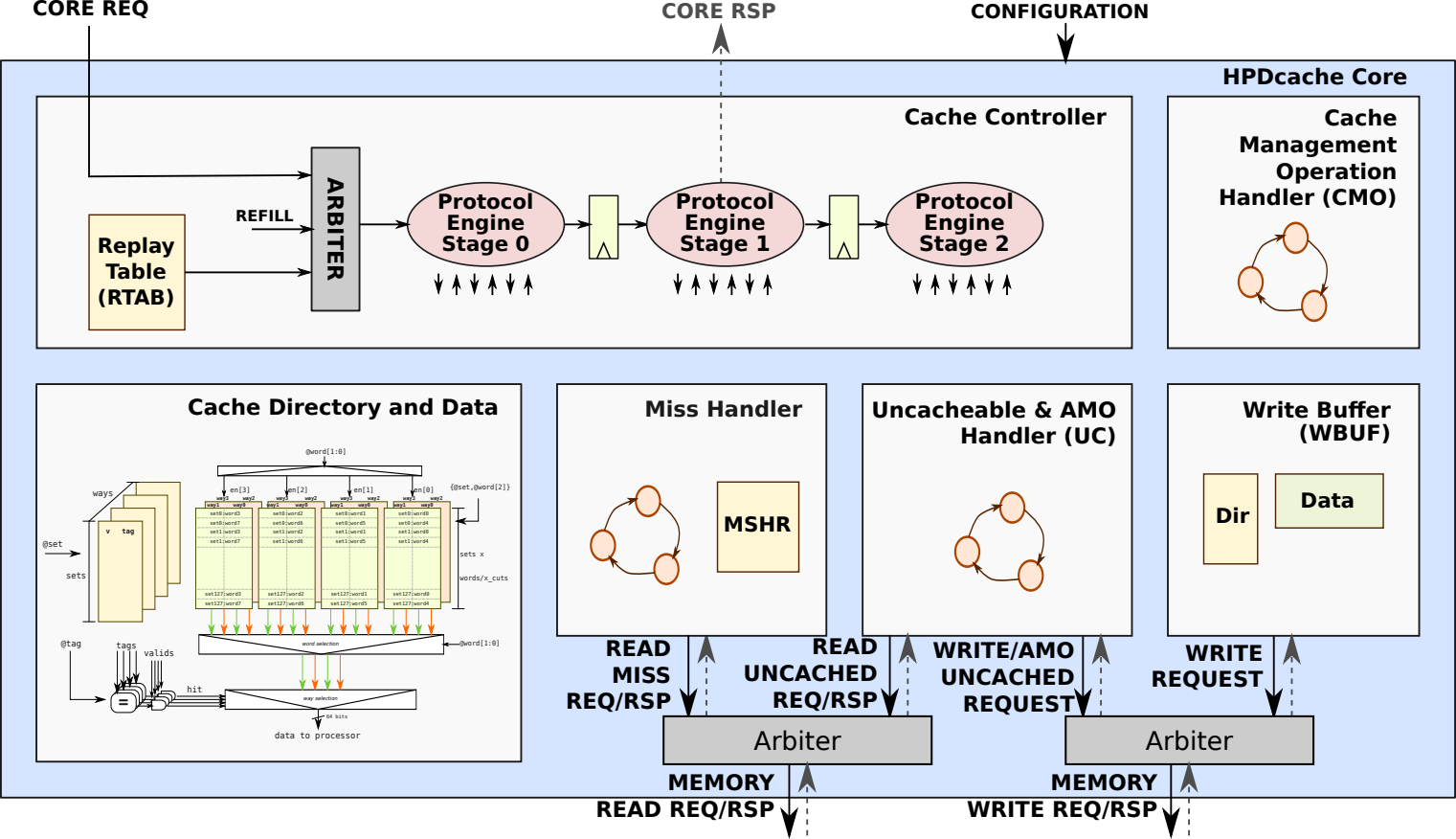
CORE REQ
CORE RSP
CONFIGURATION
HPDcache Core
Cache Controller
REFILL
ARBITER
Replay Table (RTAB)
Protocol Engine Stage 0
Protocol Engine Stage 1
Protocol Engine Stage 2
Cache Management Operation Handler (CMO)
Cache Directory and Data
ways
@set
sets
tag
@tag
tags
valids
hit
data to processor
Miss Handler
MSHR
Uncacheable & AMO Handler (UC)
Write Buffer (WBUF)
Dir
Data
READ MISS REQ/RSP
READ UNCACHED REQ/RSP
WRITE/AMO UNCACHED REQUEST
WRITE REQUEST
Arbiter
Arbiter
MEMORY READ REQ/RSP
MEMORY WRITE REQ/RSP