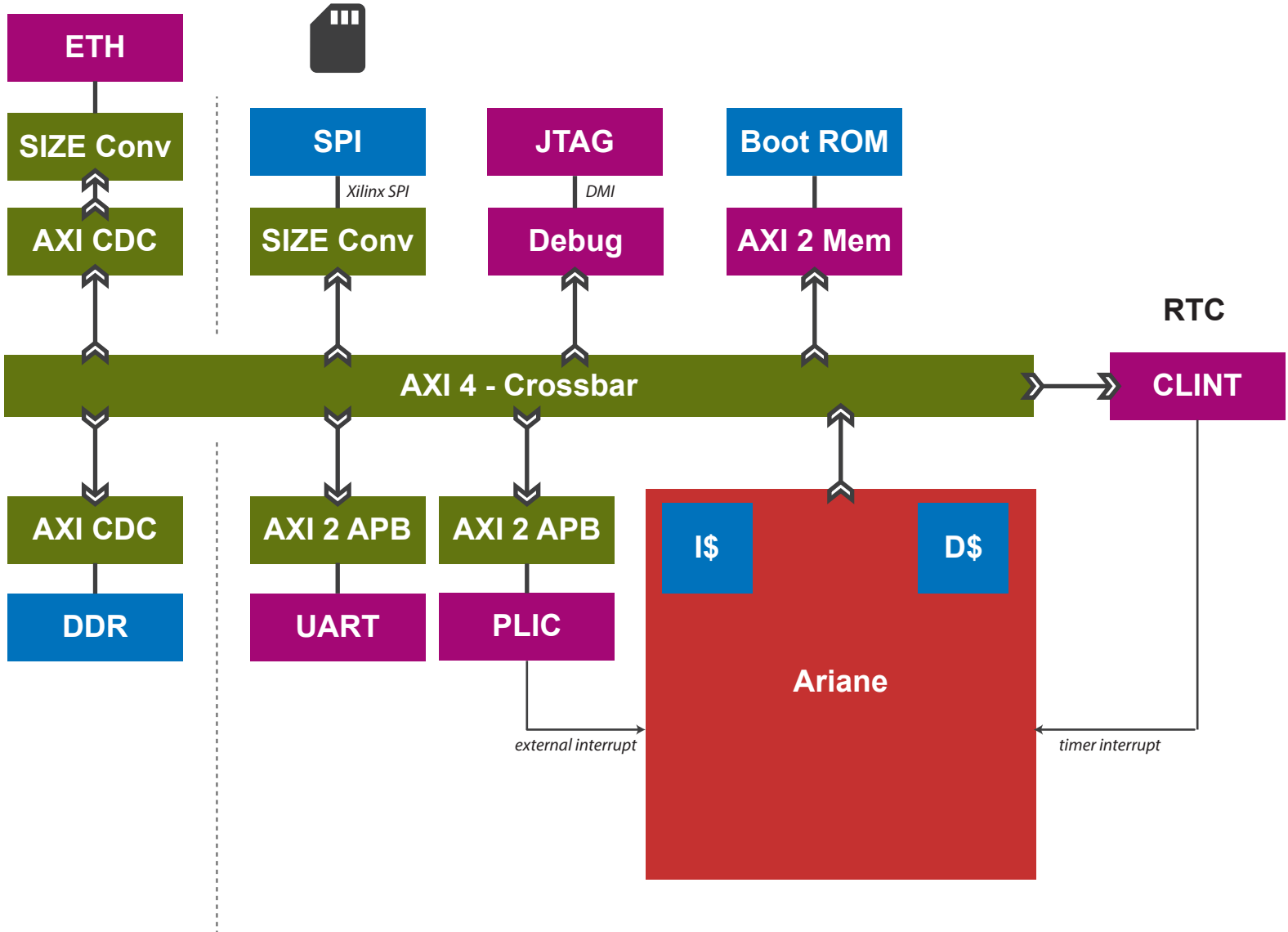
ETH
SIZE Conv
AXI CDC
SPI
Xilinx SPI
SIZE Conv
JTAG
DMI
Debug
Boot ROM
AXI 2 Mem
RTC
AXI 4 - Crossbar
CLINT
AXI CDC
DDR
AXI 2 APB
UART
AXI 2 APB
PLIC
I$
D$
Ariane
external interrupt
timer interrupt